Working in Unison with Mats

Optimizing Machine Code for Fun and Profit

Roberto Castañeda Lozano





■ Born and raised in Valencia, Spain



BSc in Spain, MSc at KTH



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 - together with Mats
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- Last five years, compiler engineer at Oracle

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- Funded by Ericsson AB and the Swedish Research Council













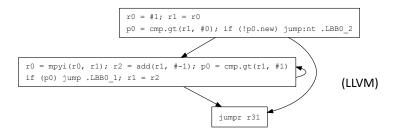








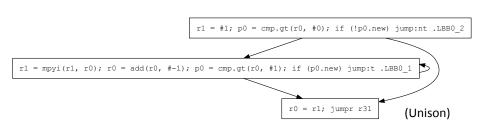
```
int fac(int n) {
  int f = 1;
  while (n > 0) {
    f = f * n;
    n--;
  return f;
```



```
r0 = #1; r1 = r0
p0 = cmp.gt(r1, #0); if (!p0.new) jump:nt .LBB0_2

r0 = mpyi(r0, r1); r2 = add(r1, #-1); p0 = cmp.gt(r1, #1)
if (p0) jump .LBB0_1; r1 = r2

(LLVM)
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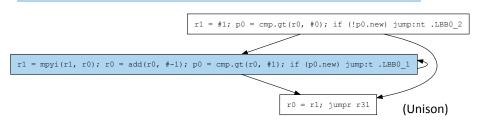


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Unison's loop is twice as fast: integrated scheduling and coalescing



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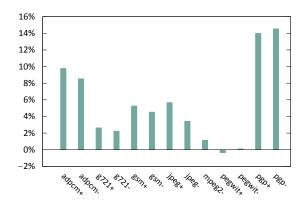
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Unison's initialization is one cycle faster: integrated scheduling and handling of calling conventions

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r0 = r1; jumpr r31 (Unison)
```

 On a larger scale: speedup over LLVM in MediaBench benchmarks (same processor)



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 - industrial use: find opportunities in production compiler

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Mats' Contributions (Cont.)

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Thank you, Mats!